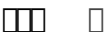
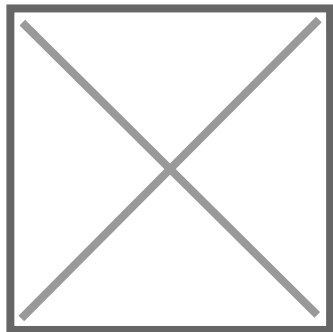




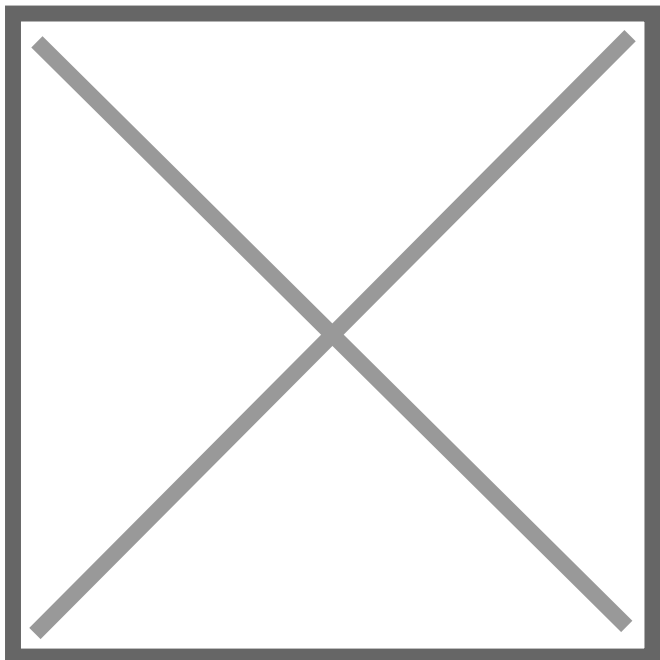
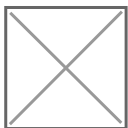
□ The Center□□ / □ Silverman AG□□

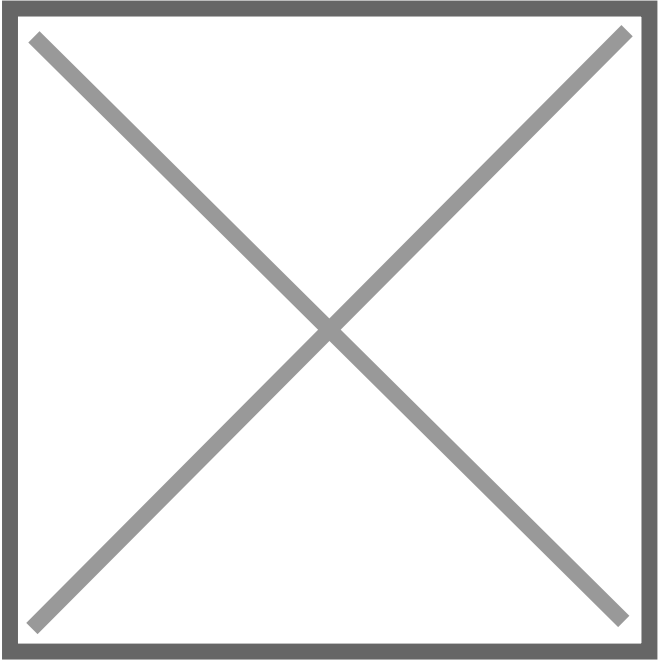


1-3	Q0
4-7	Q1
8-11	Q2
12-15	Q3
16-19	Q4
20-23	Q5
.....	



3%

simboosts ☐ ☐ ☐ ☐



☐ ☐ ☐ **0** ☐





The diagram shows a 32-bit register divided into three segments. The first segment, labeled 'Q0', contains 16 bits. The second segment, labeled '0', contains 16 bits. The third segment, labeled 'Q0', contains 16 bits. The label 'Q0' is also placed below the second segment.

[illegible]